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Most people seem to choose 10k resistors as their default pull up down value bu Jun 22 2022 TTLsolution with active low input and output. R and C are calculated for 2ms output pulse width. simulate this circuit Schematic created using CircuitLab I am planning to use this UART to RS485 module

This module is bidirectional and uses a single pair of differential signal to transmit receive data. My question is how does the chip decide whether I m a student studying electronics and I m having trouble understanding the concept behind current sourcing and current sinking

We ve covered it in a lab using a 7404 and an LED and all that. Just Point of terminology TTLserial is a severely over broad term point to point asynchronous serial atTTLlevels seems to be what you re asking about

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